



# Enhanced electrical characteristics of Zr/diamond Schottky barrier diode with cerium hexaboride interfacial layer

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## ABSTRACT

To fabricate a performance-enhanced Schottky barrier diode (SBD) on a diamond, a cerium hexaboride (CeB<sub>6</sub>) interfacial layer was introduced between the diamond and the Zr/Au electrode. The inserted CeB<sub>6</sub> layer was characterized using X-ray photoelectron spectroscopy. Introducing the CeB<sub>6</sub> layer significantly improved the performance of the diamond SBD compared with a sample without the layer. The ideality factor (n)/SBH (Φ<sub>B</sub>) values of the SBD with and without the CeB<sub>6</sub> interlayer were 2.24/1.92 eV and 2.82/1.65 eV, representing an improvement of 20.5% and 16.4% for the ideality factor and Schottky barrier height, respectively. The reverse breakdown voltage increased from -86 to -110.5 V, indicating an improvement of 28.5% for breakdown voltage. Additionally, the reverse current of the SBD with CeB<sub>6</sub> was suppressed, making it more reliable and stable than that without CeB<sub>6</sub> before the breakdown threshold. The carrier concentration and depletion layer width were increased from  $1.135 \times 10^{14} \text{ cm}^{-3}$  and 206.7 nm to  $2.646 \times 10^{14} \text{ cm}^{-3}$  and 227.6 nm, respectively, with the introduction of CeB<sub>6</sub>. These results demonstrate the advantages of inserting of CeB<sub>6</sub> in vertical diamond SBDs and show the potential of SBDs for future power rectifiers.

## 1. Introduction

Diamond is an excellent wide bandgap semiconductor for next-generation power devices due to its prominent physical and electrical properties such as high carrier mobility (electrons/ holes:  $4500/3800 \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$ ), large breakdown electric field (more than  $10 \text{ MV} \cdot \text{cm}^{-1}$ ), and high thermal conductivity ( $2200 \text{ W} \cdot \text{m}^{-1} \cdot \text{K}^{-1}$ ) [1–3]. Improvement and development of single crystal diamond growth using chemical vapor deposition techniques offer new possibilities for high-performance electronic devices, particularly diamond Schottky barrier diodes (SBDs) [4–5]. Currently, the theoretical behaviors of vertical diamond SBDs are rarely realized. However, an effective way to modify the Schottky barrier height (SBH) and improve the carriers transport across the metal–semiconductor (MS) interface is the formation of an interfacial layer [6–7]. The SBD electrical parameters, such as rectification ratio, ideality factor, reverse leakage current, and breakdown threshold, are significantly affected by inserting an interfacial layer. Nonetheless, reports on the characteristics of diamond SBDs with interfacial layers remain limited, requiring the introduction of different materials as interfacial layers to investigate diamond SBD performances.

Cerium hexaboride (CeB<sub>6</sub>) has a high melting point (2190 °C), good chemical stability, and low work function (2.5 eV). Thus, it has been widely used in thermionic electron sources [8]. Moreover, it exhibits more stability and has a lower work function than LaB<sub>6</sub> which shows an improved ability for SBDs [9]. Although CeB<sub>6</sub> has been studied in diamond field effect transistors [10], there have been few studies reported with CeB<sub>6</sub> in diamond SBDs. The excellent properties of CeB<sub>6</sub> make it a promising interfacial layer between MS layers, which can improve the interface state density and reduce defects for high-performance devices. In this study, an as-fabricated vertical Zr/CeB<sub>6</sub>/p-diamond SBD with enhanced performance was obtained, demonstrating an approach to fabricating quality Schottky contacts with diamonds.

## 2. Device fabrication and measurement

A  $3 \text{ mm} \times 3 \text{ mm} \times 0.3 \text{ mm}$  IIB-type (100)-oriented high-pressure high-temperature P<sup>+</sup> diamond substrate with a boron concentration of  $10^{20} \text{ cm}^{-3}$  was used. Before the drift layer deposition, the diamond sample was immersed in acid and alkali solution at 250 °C for 1 h to remove the nondiamonds and impurities. The epitaxial layer with a

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thickness of 400 nm was unintentionally grown on the diamond as the P diamond drift layer using microwave plasma chemical vapor deposition equipment [9]. The as-grown drift layer was then irradiated with UV/Ozone for 20 min to obtain surface oxygen. Subsequently, a Ti/Pt/Au (thickness: 40/40/120 nm) multilayer was deposited on all backside of the diamond substrate using an electron beam (EB) evaporator, followed by rapid thermal annealing treatment at 450 °C in H<sub>2</sub> for 5 min to obtain an ohmic contact. Finally, a CeB<sub>6</sub>/Zr/Au (thickness: 15/40/120 nm) diamond SBD with a round radius of 50 μm was prepared on the drift layer utilizing standard photolithography and electron beam evaporation techniques. A diamond SBD with no CeB<sub>6</sub> layer was fabricated on the same diamond substrate using the same experimental parameters to serve as a reference.

The characteristics of current/capacitance–voltage (J/C-V) for the diamond SBD were measured using an Agilent B1505A parameter analyzer. In addition, the presence of CeB<sub>6</sub> was characterized by using an X-ray photoelectron spectrometer (XPS). Fig. 1 shows side view schematics of diamond SBDs and their reference.

### 3. Results and discussion

XPS analysis was used to determine the composition of CeB<sub>6</sub>. Fig. 2 (a) shows two peaks for Ce 3d<sub>5/2</sub> and Ce 3d<sub>3/2</sub> spectra. The binding energies for Ce 3d<sub>5/2</sub> and Ce 3d<sub>3/2</sub> were estimated as 885.7 and 904.4 eV, respectively. The B 1 s spectrum in Fig. 2(b) contains boride and boron-oxides, with a peak at 187.8 eV corresponding to the boron contained in CeB<sub>6</sub>, and a peak at 191.4 eV, which might be due to the BO<sub>x</sub> of sub boron-oxides [11]. The XPS results show evidently peaks of Ce 3d and B 1 s, demonstrating a successful CeB<sub>6</sub> deposition.

Fig. 3(a) exhibits the current density–voltage (J-V) characteristics of the diamond SBD on a semilog-scale. Maximum forward current densities (J) of 97.45 and 12,738 A/cm<sup>2</sup> for SBDs with and without CeB<sub>6</sub>, respectively, at 10 V were obtained. Although the SBD without CeB<sub>6</sub> had a higher maximum forward current density, it also showed a higher reverse leakage current. The SBDs with CeB<sub>6</sub> showed good rectifying behavior with rectification ratios of  $1.503 \times 10^7$ , better than that of  $1.626 \times 10^6$  at ± 10 V for SBDs without CeB<sub>6</sub>. The insertion of CeB<sub>6</sub> resulted in an improved rectification ratio and decreased leakage current compared with the reference sample. The result implies that CeB<sub>6</sub> insertion suppresses the reverse current significantly. Based on Cheung's model [12], the ideality factor (n)/SBH (Φ<sub>B</sub>) values were calculated as 2.24/1.92 eV and 2.82/1.65 eV for SBDs with and without CeB<sub>6</sub>, respectively, a distinct improvement. Moreover, the turn-on voltages of the SBDs with and without CeB<sub>6</sub> were 2.85 and 2.15 V at 1 A/cm<sup>2</sup>, respectively, showing an increase in the turn-on voltage with the introduction of CeB<sub>6</sub>. From Fig. 3(b), the reverse breakdown voltages of the diamond SBDs with and without CeB<sub>6</sub> were measured as −110.5 and −86 V, respectively. Fig. 3(b) also indicates that the leakage current of the SBD without CeB<sub>6</sub> increased dramatically with an increase in the reverse voltage. Conversely, the SBD with CeB<sub>6</sub> exhibited a significant

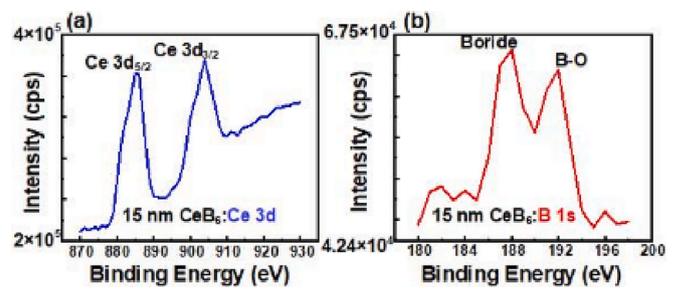


Fig. 2. XPS results of CeB<sub>6</sub> (a) Ce 3d, (b) B 1 s for the 15 nm thickness film.

increment in the leakage current only until the reverse voltage became greater than −64.5 V. The abnormal change in the leakage current may be ascribed to the recombination current resulting from the electron occupied interfacial states. The 28.5% enhancement of the reverse breakdown voltage and suppression of leakage for the SBD with CeB<sub>6</sub> suggests that introducing CeB<sub>6</sub> can improve the breakdown voltage by adjusting the SBH. The J-V curves in Fig. 3 indicate that the insertion of the CeB<sub>6</sub> layer can enhance the SBD performance. We expect further improvement with annealing treatment because the performance of an SBD with a LaB<sub>6</sub> interfacial layer is worse than that in an SBD with CeB<sub>6</sub> but becomes better than that in SBD with CeB<sub>6</sub> after annealing treatment [13].

Fig. 4 shows the C<sup>-2</sup>-V properties of the vertical diamond SBD with CeB<sub>6</sub> and the reference SBD at 500 KHz. The carrier concentrations (N<sub>A</sub>) of SBDs with and without CeB<sub>6</sub> were estimated as  $2.646 \times 10^{14}$  and  $1.135 \times 10^{14}$  cm<sup>-3</sup>, respectively. Additionally, the widths of the depletion region were calculated as 227.6 and 206.7 nm for SBDs with and without CeB<sub>6</sub>, respectively. The discrepancies between the carrier concentrations for SBDs with and without CeB<sub>6</sub> are mainly due to the existence of introduced interfacial states [9]. According to the C-V results, the values of V<sub>bi</sub>/SBH were calculated as 4.064/4.322 V and 1.915/2.141 V for the SBD with and without CeB<sub>6</sub>, respectively. The values calculated from the C-V results are higher than those from the I to V results which can be explained by the existence of excess capacitance in the devices due to CeB<sub>6</sub> or interface states at the Zr/CeB<sub>6</sub>/diamond interface. And other explanation is the inhomogeneity of the Schottky interface [14].

### 4. Conclusions

Vertical diamond SBDs with and without CeB<sub>6</sub> were fabricated and characterized. The results demonstrated improved performances when introducing the CeB<sub>6</sub> interfacial layer. The SBDs with and without CeB<sub>6</sub> showed rectification ratios of  $1.503 \times 10^7$  and  $1.626 \times 10^6$  at ± 10 V, respectively. The device with CeB<sub>6</sub> demonstrated a 20.5% improvement in ideality factor and a 16.4% increase in SBH compared with the values obtained without CeB<sub>6</sub>. In addition, the reverse breakdown voltage was enhanced from −86 to −110.5 V with the introduction of the CeB<sub>6</sub> layer, and the leakage current was suppressed. These results suggest that the CeB<sub>6</sub> layer can potentially improve the performance of high-power electronics.

### CRedit authorship contribution statement

**Tian-Fei Zhu:** Conceptualization, Data curation, Formal analysis, Investigation, Methodology, Visualization, Writing – original draft, Visualization, Funding acquisition. **Guoqing Shao:** Investigation, Methodology, Data curation. **Tai Min:** Supervision. **Hong-Xing Wang:** Resources, Supervision, Writing – review & editing, Project administration, Funding acquisition.

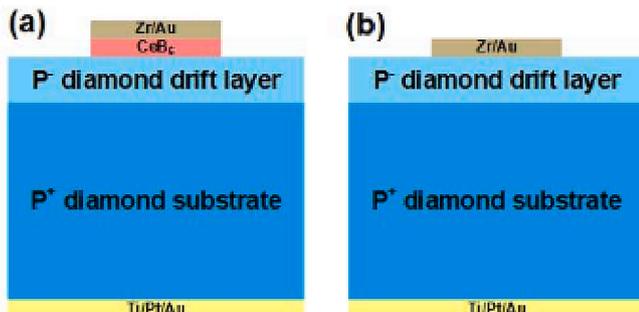


Fig. 1. Schematic diagrams of diamond SBD (a) with CeB<sub>6</sub> and (b) reference sample.

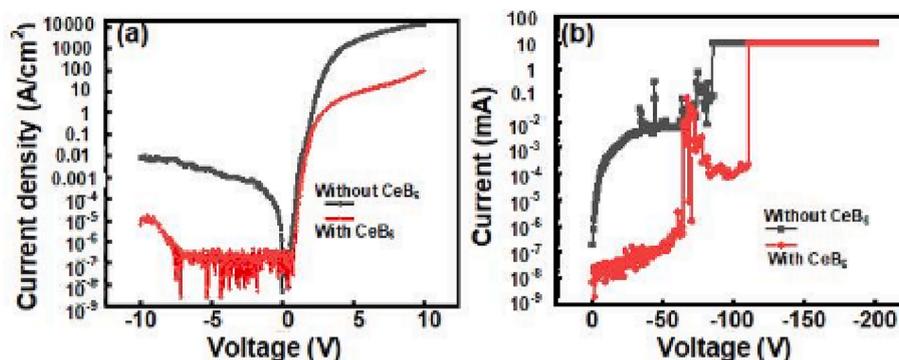


Fig. 3. (a) J-V characteristics on semilog-scale and (b) reverse semilog I-V characteristics of diamond SBD with CeB<sub>6</sub> and its reference, respectively.

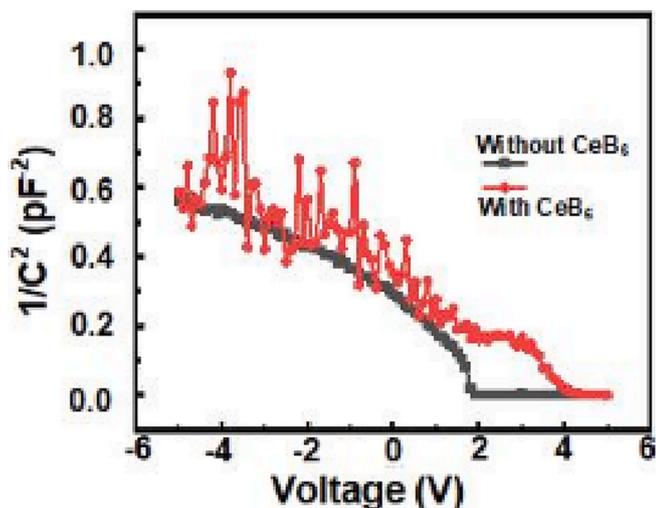


Fig. 4. C<sup>2</sup>-V characteristics of diamond SBDs with and without CeB<sub>6</sub> at 500 KHz.

#### Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

#### Data availability

Data will be made available on request.

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